

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yuichi SATO

Serial No.: Unassigned

Filing Date: October 5, 1999

For: STATIC RANDOM ACCESS MEMORY
AND SEMICONDUCTOR DEVICE
USING MOS TRANSISTORS HAVING
CHANNEL REGION ELECTRICALLY
CONNECTED WITH GATE

Examiner: Unassigned

Group Art Unit: Unassigned



**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR 1.97 and 1.98, applicant submits for consideration the document listed on the attached Form PTO-1449. A copy of the document is also submitted herewith. The Examiner is requested to make this document of record.

This Information Disclosure Statement is submitted with the filing of a new U.S. patent application. No fee is required.

Applicant would appreciate the Examiner initialing and returning the copy of the Form PTO-1449 indicating that the information has been considered and made of record herein.

This Information Disclosure Statement under 37 CFR 1.97 is not to be construed as a representation that: (I) a complete search has been made; (ii) additional information material to

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the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.


In the event that the transmittal letter is separated from this document and the Patent and Trademark Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952**. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Respectfully submitted,

Dated: October 5, 1999

By:

for


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